

# Message from the Head, Electrical Engineering

## **A brief on Research Assistant Programme**

The Department of Electrical Engineering admits a number of Research Assistants (RAs) and Project Assistants (PAs) every year. These students also register for the M.Tech. degree. The duration of this programme is 3 years. Their M.Tech curriculum differs from M.Tech. (Teaching Assistants) students only in terms of number of credits earned per semester.

**Institute RAs** are assigned duties in various teaching laboratories of the department like basic electronics, microprocessors, analog and digital circuits, machines and electrical drives etc. The primary function of RA is to work with Professor in-charge of the respective laboratory for day to day functioning of the lab including maintenance of the equipments, system administration and organizing experimental set-up. Institute RAs may also be assigned other academic or administrative duties related to conduct of examinations, admissions process etc by the Head of the department.

**Project RAs** are assigned duties in sponsored research projects being undertaken in the department by various faculty members. The primary function is to assist the Professor in-charge or Principal Investigator of the respective project either on a research project or any academic or administrative work related to the project. Normally, it is expected that the students will be carrying out his/her research as a part of MTech thesis in the same project.

Both Institute RA and Project RA are expected to spend about 20 hours per week towards their assigned duties in addition to working towards fulfilment of academic requirements of MTech.

Following is the schedule for M.Tech. Written Test and/or Interview:

**Thursday, 7<sup>th</sup> June 2018**

<i>Event</i>	<i>Reporting Time</i>	<i>Venue</i>
Reporting time & Address by the Head	8:30 am sharp	LA 102, Lecture Hall complex
Written Test	9:00 am to 12:00 pm (3 hours)	LA 102, Lecture Hall complex
Project Presentation by the faculty members (for PRA positions)	2:00 pm	LA 102, Lecture Hall complex
Written Test Result declaration	10:00 pm	Department website & Notice boards of Girish Gaitonde Bldg., EE Dept.

**Friday, 8<sup>th</sup> June 2018**

<i>Event</i>	<i>Reporting Time</i>	<i>Venue</i>
Address by the Head to shortlisted candidates for interview	8:30 am sharp	EEG 101, Girish Gaitonde Bldg.
Interviews & Lab Skill Test (for Electronic Circuit Lab, System Administration, Electrical Machine Lab)	9:00 am onwards	Respective labs/GG Conf Room

**Saturday, 9<sup>th</sup> June 2018**

<i>Event</i>	<i>Reporting Time</i>	<i>Venue</i>
Interviews (if required)	10:00 am onwards	GG Conf. Room

The candidates called for Written Test and Interview would have to fill a preference form by **June 4, 2018**. ([www.ee.iitb.ac.in/raadmission2018](http://www.ee.iitb.ac.in/raadmission2018)) indicating their choice for the skill test. This portal will be open from **May 25, 2018**. For any queries regarding the preference form, please email to <raadmin@ee.iitb.ac.in>.

For syllabi of the written test & sample question paper, please visit the following link. Candidates are requested to review this carefully to make an informed decision about the specialization.

[www.iitb.ac.in/admissions](http://www.iitb.ac.in/admissions) or

[www.ee.iitb.ac.in/raadmission2018](http://www.ee.iitb.ac.in/raadmission2018)